**Lab 8-1**

1. **Completed Truth Table:**

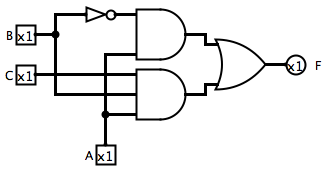
|  |  |  |  |
| --- | --- | --- | --- |
| **ANDZ** | **Z-Current Value** | **ALU Zout** | **OUTPUT**  **Z-New Value** |
| 0 | (doesn’t matter) | 0 | 1 |
| 0 | (doesn’t matter) | 1 | 1 |
| 1 | 0 | 0 | 0 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 |

1. **Boolean Expression:**

(ANDZ = A, B = Z-Current Value, C = ALU Zout)

F = (A’B)+ (AB C)’)

1. **Logic Gate Diagram:**

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